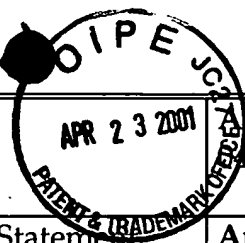


Form PTO-1449 (Modified)



Att'y Docket No.:

2390.P10071

Serial No.:

09/752,594

List of Patents and Publications Statement

(Use several sheets if necessary)

Applicants:

Terry Kendall, et al.

Filing Date:

December 27, 2000

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

Exam. Initial	Date	Document Number	Name	Class	Sub-Class	Filing Date
	10/2087	4,701,745	Waterworth	341	67	
↑	5/14/91	5,016,009	Whiting, et al.	341	67	
	10/1/91	5,053,990	Kreifels, et al.	711	103	
	7/26/94	5,333,300	Fandrich	711	103	
	5/2/95	5,412,793	Kreifels, et al.	365	185.33	
	9/5/95	5,448,712	Kynett, et al.	711	103	
	4/16/96	5,509,134	Fandrich, et al.	711	103	
	11/25/97	5,692,138	Fandrich, et al.	711	103	
	3/17/98	5,729,489	Fazio, et al	365	185.03	

FOREIGN PATENT DOCUMENTS

No.	Document No.	Date	Country	Name	Class	Sub-Class	Translation

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

	"Simplify Manufacturing by Using Automatic Test-equipment for On-Board Programming," Intel Corporation, AP-629 Application Note, pp 1-15, December 1998.
	"3 Volt Fast Boot Block Flash memory 28F800F3 and 28F160F3," Intel Corporation, Doc # 290644-005, pp 1-46, January 2000.
	"Improving Programming Throughput of Automated Flash Memories," Intel Corporation, AP-678 Application Note, pp 1-22, December 1998.

Examiner

Date Considered

06-09-03

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant

Substitute for Form 1449A/PTO (Modified)

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known

Application Number	09/752,594
Filing Date	December 27, 2000
First Named Inventor:	Terry L. Kendall
Group Art Unit	2818
Examiner Name	
Attorney Docket Number	42390P10071

Sheet 1 of 2

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

**Examiner
Signature**

Date Considered
06-09-03

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